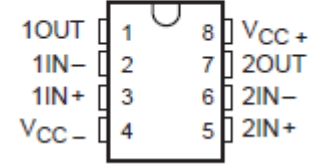
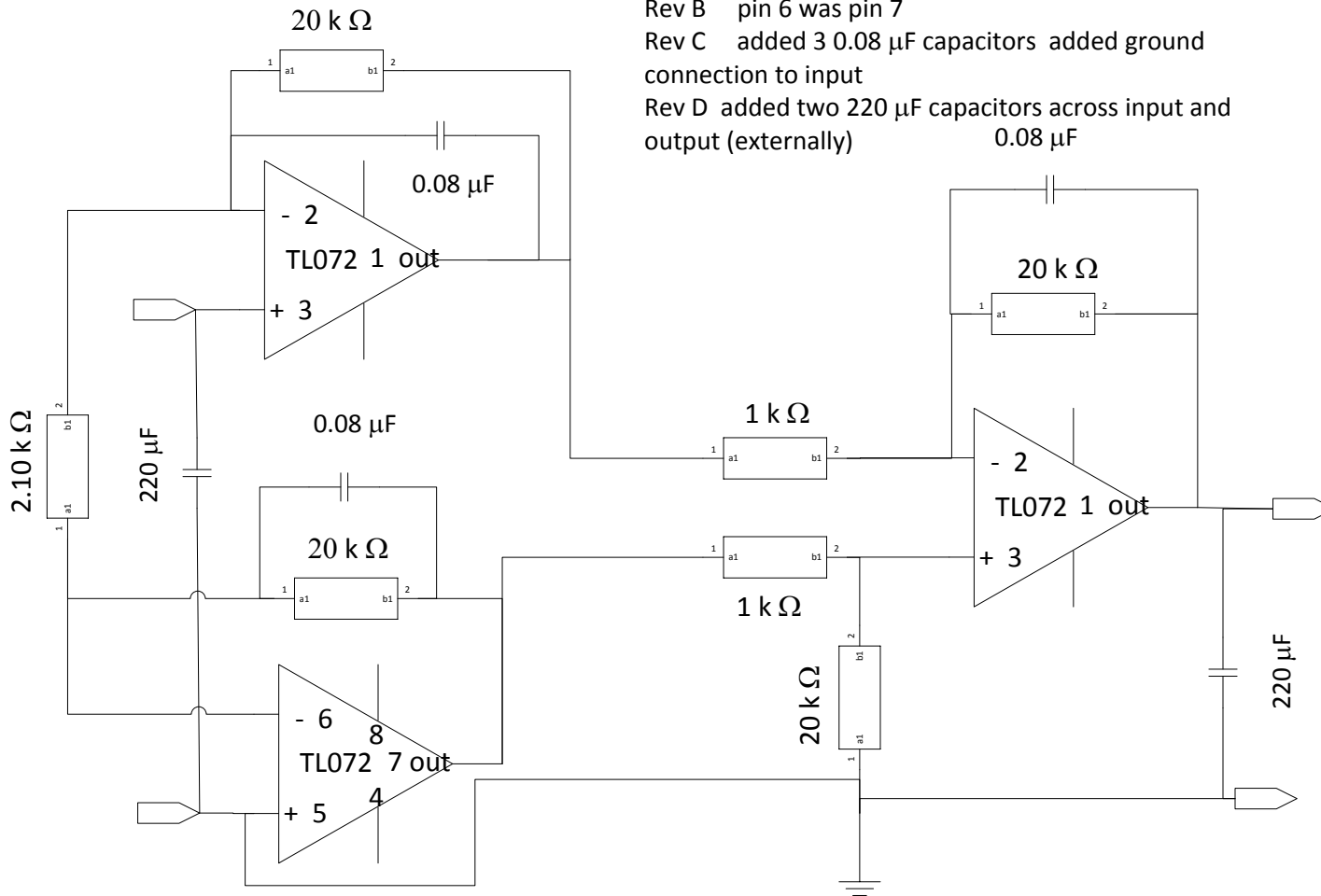


Terminal 8 is V_{cc+} , Terminal 4 is V_{cc-} for all chips. Power is ± 10 Vdc. Overall gain is 400.

TL072, TL072A, TL072B
D, JG, P, OR PW PACKAGE
(TOP VIEW)



- Rev A revised resistor 20 k Ω was 10 k Ω
- Rev B pin 6 was pin 7
- Rev C added 3 0.08 μ F capacitors added ground connection to input
- Rev D added two 220 μ F capacitors across input and output (externally) 0.08 μ F



First stage gain: $A_1 = 1 + 2 \cdot \frac{R_{F1}}{R_1}$

Low Pass Filter, 100 Hz cutoff

$$f_c = \frac{1}{2 \cdot \pi \cdot R \cdot C} \quad C = \frac{1}{2 \cdot \pi \cdot R \cdot f_c} \quad \frac{1}{2 \cdot \pi \cdot 100 \cdot \text{Hz} \cdot R_{F2}} = 0.08 \mu\text{F}$$

$$20 = 1 + 2 \cdot \frac{R_{F1}}{R_1} \xrightarrow{\text{solve, } R_1} \frac{2 \cdot R_{F1}}{19} \quad \frac{40}{19} = 2.105$$

Second stage gain $A_2 = \frac{R_{F2}}{R_2}$